



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jing-Rung Wang : Group
 Serial No: 10/609,387 : Art Unit #2138
 Filed: 1 July 2003 : Examiner:
 Title: METHOD FOR TESTING CHIP
 CONFIGURATION SETTINGS : C.H. Britt

AMENDMENT

Mail Stop – Amendments
 Honorable Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

The Examiner issued a non-final Office Action dated 10 February 2006 in the above-referenced Patent Application. In that Office Action the Examiner set a three-month shortened statutory period for reply. That reply period having now expired, a Request and Fee for one-month Extension of Time is concurrently filed herewith to ensure timely filing.

Responsive to the Office Action dated 10 February 2006, please amend the above-referenced Patent Application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the Listing of Claims which begins on page 11 of this paper.

Remarks/Arguments begin on page 16 of this paper.

MR3003-47
Serial Number: 10/609,387
Reply to Office Action dated 10 February 2006

An **Appendix** which includes a Substitute Specification is attached following page 19 of this paper.